We Claim:

1. A timing control configuration of a transmitting and/or receiving device for a mobile station in a communications system, wherein the transmitting and/or receiving device is configured to transmit/receive signals of different mobile radio standards with differing time patterns, the timing control configuration comprising:

a system clock generator for producing a standard system clock for the differing time patterns;

a clock divider connected to receive the standard system clock, said clock divider having a control input for setting a mean period duration of an output clock, and said clock divider generating an output clock corresponding to one of the differing time patterns, in accordance with at least one control parameter received at said control input; and

an event controller connected to said clock divider and receiving the output clock, said event controller effecting a time control of events based on the output clock and event information.

2. The configuration according to claim 1, wherein a clock frequency of the system clock is not a common multiple of clock frequencies defined in the differing time patterns.

3. The configuration according to claim 1, wherein said clock divider is configured to derive clock frequencies of the differing time patterns from a clock frequency of the system clock using a division factor in the form K/L, where K and L are positive integers, and K < L, and where L is not a power to base 2.

4. The configuration according to claim 1, wherein said clock divider is configured to derive clock frequencies of the differing time patterns from a clock frequency of the system clock using an arbitrary rational division factor K/L, where K and L are positive integers, and K < L.

5. The configuration according to claim 1, wherein said clock divider is a completely digital clock divider.

6. The configuration according to claim 5, wherein said digital clock divider includes a phase accumulator using the system clock or a clock derived therefrom to add a phase increment having a programmable value.

7. The configuration according to claim 5, wherein said digital clock divider includes a fractional clock divider allowing exact clock division averaged over time.

8. The configuration according to claim 7, wherein:

said fractional clock divider has a register capable of being incremented and capable of being decremented;

with a division factor of K/L, said register is incremented using the system clock or a clock derived therefrom with a value $\Delta INC = 2K$, provided that the value held in said register is less than 0, and said register is otherwise decremented with the value $\Delta DEC = 2 \cdot (K-L)$, where K and L are positive integers and $K < L$; and

the output clock is determined by a change in a mathematical sign of a value held in said register.

9. The configuration according to claim 5, which comprises an initial divider with an integer divisor preceding said clock divider in a signal flow direction.

10. The configuration according to claim 9, wherein the divisor is not a power to base 2.

11. The configuration according to claim 1, which comprises a resettable counter for counting the output clock; and wherein said event controller includes a table memory containing

events associated with counts dependent on a chosen time pattern, and wherein said event controller is configured to initiate an event when the resettable counter has reached a count associated with the respective event.

12. The configuration according to claim 11, wherein said event controller is configured to produce a reset signal for resetting the resettable counter based on the chosen time pattern.

13. The configuration according to claim 1, wherein said clock divider is configured to produce time patterns based on at least two standards selected from the group consisting of GSM or EDGE, and TIA/EIA-136 and UMTS.

14. A timing control method for a transmitting and/or receiving device in a mobile station of a communications system, wherein the transmitting and/or receiving device is configured to transmit/receive signals of different mobile radio standards with differing time patterns, the method which comprises the following steps:

producing a common standard system clock for the differing time patterns;

producing an output clock corresponding to one of the differing time patterns on the basis of the standard system clock by presetting at least one control parameter via which the mean period duration of the output clock can be set; and

controlling a timing of events based on the output clock and event information.